

## TelComSoft's-Stereocoder

This schematic was originally drawn by Reuver from TelComSoft and was published in an old RadioBulletin magazine. The multiplexer consist of 4 TTL ICs. The 7600 kHz-crystaloscillator has 3 NAND ports, that are used as analog amplifier. To overcome part spread these NAND ports should all be in the same IC. 7600 kHz crystals can be obtained in many dump shops and some partsuppliers (e.g. Radio Ster in The Hague). The 7600 kHz frequency is 2 times divided by 10 (IC 2 and 3). This signal (76 kHz) is led to 2 cascaded D-FlipFlops, that function as divide-by-two. The 38 kHz signal is taken from the Q and from the Qnot outputs. Now T1 and T2 will alternately conduct, making the emittervoltages 0.6V below the highsest basisvoltage. T3 is added to improve the linearity. The 38kHz signal can be optimal suppressed by trimming R15. The 19kHz pilottone is added in the end, amplitude of the pilote tone can be adjusted by trimming R13.









